

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10811835			
<b>Filing Date:</b>	30-Mar-2004			
<b>Title of Invention:</b>	Method of designing a circuit layout of a semiconductor device			
<b>First Named Inventor/Applicant Name:</b>	Kazuaki Goto			
<b>Filer:</b>	Donald R. Studebaker/Pamela Patrick			
<b>Attorney Docket Number:</b>	030712-29			
Filed as Large Entity				
<b>Utility under 35 USC 111(a) Filing Fees</b>				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
Notice of appeal	1401	1	540	540
<b>Post-Allowance-and-Post-Issuance:</b>				
<b>Extension-of-Time:</b>				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension - 3 months with \$0 paid	1253	1	1110	1110
<b>Miscellaneous:</b>				
<b>Total in USD (\$)</b>				<b>1650</b>